

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants:	Ting, <i>et al.</i>	Docket No.:	TSM03-0945
Serial No.:	10/840,125	Art Unit:	2811
Filed:	May 6, 2004	Examiner:	Nadav, Ori
For:	Notched Spacer for CMOS Transistors		

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**RESPONSE UNDER 37 C.F.R. § 1.116**

Dear Sir:

Applicants respectfully submit the following amendments and remarks in response to Examiner's Office Action dated December 3, 2008, which Action is made final. Applicants respectfully request that these amendments and remarks be entered pursuant to the provisions of 37 C.F.R. § 1.116, and respectfully request reconsideration of the claims.

OK TO ENTER: /O.N./